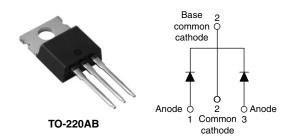


Vishay High Power Products

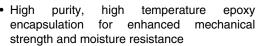
Schottky Rectifier, 2 x 20 A



PRODUCT SUMMARY					
I _{F(AV)}	2 x 20 A				
V _R	30 V				

FEATURES

- 150 °C T_J operation
- Center tap configuration
- Very low forward voltage drop





- · High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Lead (Pb)-free ("PbF" suffix)
- Designed and qualified for industrial level

DESCRIPTION

This center tap Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICSL					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	40	A		
V_{RRM}		30	V		
I _{FSM}	$t_p = 5 \mu s \text{ sine}$	1100	Α		
V _F	20 Apk, T _J = 125 °C (per leg)	0.38	V		
T_J	Range	- 55 to 150	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	42CTQ030PbF	UNITS	
Maximum DC reverse voltage	V _R	30	V	
Maximum working peak reverse voltage	V_{RWM}	30	V	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	L TEST CONDITIONS VALU		VALUES	UNITS
Maximum average per leg		50 % duty cycle at T _C = 121 °C, rectangular waveform		20	
See fig. 5 per device	I _{F(AV)}			40	A
Maximum peak one cycle		0 p.0 00 0. 0 p.0 .00 p00	Following any rated load condition and with rated	1100	
non-repetitive surge current per leg See fig. 7	I _{FSM}	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	360	
Non-repetitive avalanche energy per leg E _{AS}		T _J = 25 °C, I _{AS} = 3 A, L = 2.90 mH		13	mJ
Repetitive avalanche current per leg I _{AR}		Current decaying linearly to zero in 1 μ s Frequency limited by T_J maximum $V_A = 1.5$ x V_R typical		3	А

^{*} Pb containing terminations are not RoHS compliant, exemptions may apply

42CTQ030PbF

Vishay High Power Products Schottky Rectifier, 2 x 20 A



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop per leg See fig. 1	V _{FM} ⁽¹⁾	20 A	T _J = 25 °C	0.48	V
		40 A		0.57	
		20 A	T _J = 125 °C	0.38	
		40 A		0.51	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	3	mΛ
See fig. 2	IRM ('')	T _J = 125 °C		183	mA mA
Threshold voltage	V _{F(TO)}	$T_J = T_J$ maximum		0.22	V
Forward slope resistance	r _t			6.76	mΩ
Maximum junction capacitance per leg	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		2840	pF
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		8.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storag temperature range	е	T _J , T _{Stg}		- 55 to 150	°C
Maximum thermal resistance, junction to case per leg		D	DO securities	2.0	
Maximum thermal resistance, junction to case per package		R _{thJC}	DC operation	1.0	°C/W
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.50	
Approximate weight				2	g
				0.07	OZ.
Mounting torque ——	minimum			6 (5)	kgf ⋅cm
	maximum			12 (10)	(lbf \cdot in)
Marking device			Case style TO-220AB	42CTQ030	

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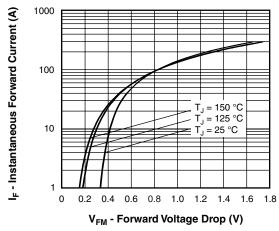


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

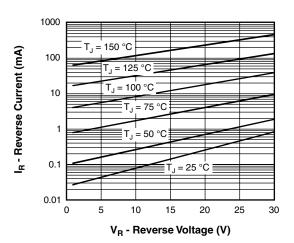


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

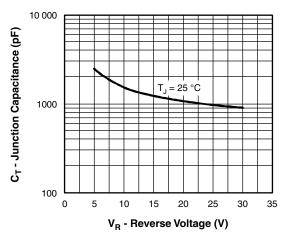


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

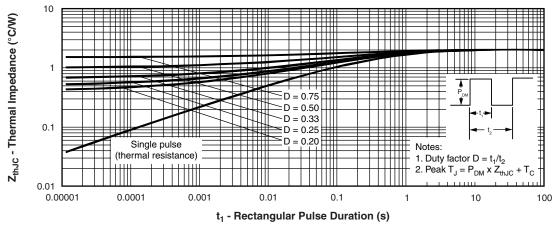


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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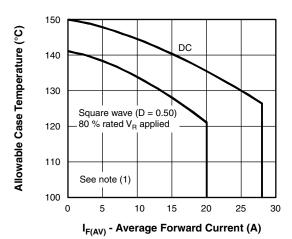


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

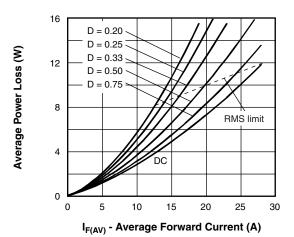


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

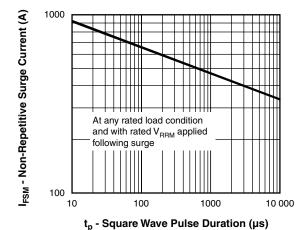


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

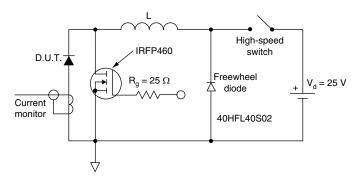


Fig. 8 - Unclamped Inductive Test Circuit

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; Pd = Forward power loss = $I_{F(AV)} \times V_{FM}$ at ($I_{F(AV)}/D$) (see fig. 6); Pd_{REV} = Inverse power loss = $V_{R1} \times I_R$ (1 - D); I_R at V_{R1} = 10 V

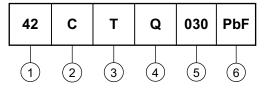
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ORDERING INFORMATION TABLE

Device code



- 1 Current rating (40 A)
- 2 Circuit configuration:

C = Common cathode

Package:

T = TO-220

- 4 Schottky "Q" series
- 5 Voltage rating (030 = 30 V)
- 6 • None = Standard production
 - PbF = Lead (Pb)-free

Tube standard pack quantity: 50 pieces

LINKS TO RELATED DOCUMENTS			
Dimensions http://www.vishay.com/doc?95222			
Part marking information	http://www.vishay.com/doc?95225		

Document Number: 94220 Revision: 13-Aug-08



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